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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/514,369	02/28/2000	Takayuki Shinohara	49657-625	9468		
20277 7	7590 03/24/2003					
MCDERMOTT WILL & EMERY			EXAMI	EXAMINER		
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			ART UNIT	PAPER NUMBER		
			2684	11		
		·	DATE MAILED: 03/24/2003	t 1		

Please find below and/or attached an Office communication concerning this application or proceeding.

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. v	A	pplication No.		Applicant(s)	<u></u>
		9/514,369		SHINOHARA ET AL.	
Office Action Su	mmary	xaminer		Art Unit	
	Т	. Richard Lei		2684	
The MAILING DATE of t Period for Reply	his communication appea	rs on the cove	sheet with the c	orrespondence ad	dress
A SHORTENED STATUTORY THE MAILING DATE OF THIS - Extensions of time may be available und after SIX (6) MONTHS from the mailing - If the period for reply specified above is - If NO period for reply is specified above, - Failure to reply within the set or extende - Any reply received by the Office later tha earned patent term adjustment. See 37 that	COMMUNICATION. er the provisions of 37 CFR 1.136(a date of this communication. ess than thirty (30) days, a reply wit the maximum statutory period will a d period for reply will, by statute, cau n three months after the mailing dat). In no event, howen nin the statutory mir pply and will expire use the application to	ever, may a reply be tim imum of thirty (30) days SIX (6) MONTHS from to become ABANDONED	ely filed will be considered timely the mailing date of this co (35 U.S.C. § 133).	
1) Responsive to commun	nication(s) filed on <u>24 Dec</u>	ember 2002 8	24 January 200	<u>)3</u> .	
2a) This action is FINAL.	2b)☐ This a	action is non-fi	nal.		
	in condition for allowanc ith the practice under <i>Ex</i>				e merits is
4)⊠ Claim(s) <u>1-11</u> is/are per	in the application.				
4a) Of the above claim(s) is/are withdrawn	from consider	ation.		
5) Claim(s) is/are all	owed.				
6)⊠ Claim(s) <u>1-11</u> is/are reje	cted.				
7) Claim(s) is/are ob	jected to.				
8) Claim(s) are subj	ect to restriction and/or el	ection require	ment.		
Application Papers					
9)☐ The specification is object	ted to by the Examiner.				
10)☐ The drawing(s) filed on _		-	<u>-</u>		
· · · · · · · · · · · · · · · · · · ·	t that any objection to the dr	-, ,	•	` ,	
11) The proposed drawing co				ved by the Examina	er.
	wings are required in reply t		tion.		
12)☐ The oath or declaration is	objected to by the Exam	iner.			
Priority under 35 U.S.C. §§ 119 a	ind 120			-	
13)⊠ Acknowledgment is mad	e of a claim for foreign pr	iority under 35	U.S.C. § 119(a)	-(d) or (f).	,
a)⊠ All b)□ Some * c)□	None of:			•	
1.⊠ Certified copies of	the priority documents ha	ave been rece	ived.	•	
2. Certified copies of	the priority documents ha	ave been rece	ived in Applicatio	on No	
3. Copies of the certi application fro * See the attached detailed	fied copies of the priority m the International Burea Office action for a list of t	u (PCT Rule 1	7.2(a)).		Stage
14)☐ Acknowledgment is made	of a claim for domestic p	iority under 3	5 U.S.C. § 119(e) (to a provisional	application).
a) ☐ The translation of the 15)☐ Acknowledgment is made					·
Attachment(s)					
1) Notice of References Cited (PTO-89 2) Notice of Draftsperson's Patent Drav 3) Information Disclosure Statement(s)	ving Review (PTO-948)	4)		(PTO-413) Paper No(atent Application (PT0	

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DETAILED ACTION

Response to Arguments

1. Applicant's argument and amendments filed on 24 December 2002 and 24 January 2003 have been fully considered but they are not persuasive for the following reasons.

Applicant argues that Kuroda reference does not disclose a signal transmission/reception portion for transmitting and receiving a signal, and a control portion for controlling at least a signal transmission and reception operation of the transmission/reception portion. The Applicant is advised to look at Fig.41, which shows the internal structure of a wireless telephone. Any wireless device must have transmitting and receiving circuit in order to function, whether it is explicitly shown or lumped in a block diagram described as the "high-frequency radio unit". Also the control part is indicated by the inclusion of the "CPU" in Fig.41.

Applicant further argues that Kuroda does not teach or suggest the claimed file storage flash memory for storing program for the control portion. This is not true. Kuroda repeatedly teaches that the flash memory stores not only the data, but also the program to be executed by the CPU (Col.5, Lines 30-33; Col.6, Lines 21-23).

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This Office Action does not rely on the combination of Kuroda and Robinson for the claim rejection. The Kuroda reference alone is sufficient, because Kuroda also teaches the use of RAM as work area memory and data buffer area.

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Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the

basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or

in public use or on sale in this country, more than one year prior to the date of application for

patent in the United States.

3. Claims 1-11 are rejected under 35 U.S.C. 102(b) as being anticipated by Kuroda et al.

(U.S. Patent 5,444,664).

Regarding Claim 1, Kuroda teaches a memory system for a portable telephone (Col.5,

Lines 11-14; Fig. 41) including a signal transmission/reception portion for transmitting and

receiving a signal (Fig.41, High-Frequency Radio Unit 103) and a control portion (Fig.41,

Control Unit in Block 102) for controlling at least a signal transmission and reception operation

of said transmission/reception portion, comprising: a random access memory (Col.6, Lines 17-

23) providing a working area for said control portion; and a file storage flash memory (Fig. 41,

FLASH) for storing a program for said control portion and at least transmission and reception

data in a non-volatile manner under a control of said control portion (Col.6, Lines 21-23, desired

data and program).

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Regarding Claim 2, Kuroda teaches the memory system for the portable telephone according to claim 1, wherein said random access memory and said file storage flash memory are coupled to an internal bus (Figs. 1 & 41; Col.29, Lines 9-12) interconnecting said control portion and said signal transmission/reception portion.

Regarding Claim 3, Kuroda teaches the memory system for the portable telephone according to claim 2, further comprising a bus converting circuit (Fig.43, I/O; and Col.34, Lines 27-33) connected between said file storage flash memory and said internal bus and functioning as an interface circuit for said file storage flash memory.

Regarding Claim 4, Kuroda teaches the memory system for the portable telephone according to claim 3, wherein said file storage flash memory and said bus converting circuit are integrally formed into a memory card (Fig. 43; Col.34, Lines 11-13) attachable and detachable to and from said portable telephone.

Regarding Claim 5, Kuroda teaches the memory system for the portable telephone according to claim 3, wherein said file storage flash memory is constituted of a memory card (Fig. 43; Col.34, Lines 11-13) being attachable and detachable to and from said bus converting circuit.

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Regarding Claim 6, Kuroda teaches the memory system for the portable telephone according to claim 1, wherein said control portion, said random access memory and said file storage flash memory are integrally formed as a control unit (Fig.41, and Col.6, Lines 10-23).

Regarding Claim 7, Kuroda teaches the memory system for the portable telephone according to claim 1, wherein said file storage flash memory comprises an AND type flash memory (Col.9, Lines 43-47).

Regarding Claim 8, Kuroda teaches the memory system for the portable telephone according to claim 5, wherein said bus converting circuit is formed into an adapter attachable and detachable to said portable telephone (Col.34, Lines 34-35, Note: replaceable indicates that it is attachable and detachable).

Regarding Claim 9, Kuroda teaches the memory system for a portable telephone according to claim 1, wherein said file storage flash memory comprising a plurality of sectors for storing a program for said control portion (Figs.37-39) and at least transmission and reception data in a non-volatile manner under a control of said control portion, and serially reading out a program to the random access memory to execute it (Col.5, Lines 30-32, Note that "the stored information to be processed by the CPU" indicates that the stored program bits are read out from the memory and into the CPU in a serial fashion as shown in Fig.28).

Regarding Claim 10, see Claim 1 and Claim 9 for Kuroda's teaching.

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Regarding Claim 11, see Claim 1 for Kuroda's teaching.

4. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Robinson et al. (U.S. Patent 6,154,788) teaches a flash memory device with the controller.

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Jha et al. (U.S. Patent 6,407,949) teaches a mobile communication device with integrated embedded FLASH and SRAM memory.

Jigour et al. (U.S. Patent 5,877,975) teaches an insertable/removable digital memory apparatus.

Robinson (U.S. Patent 6,260,102) teaches an interface for flash EEPROM memory arrays.

Robinson (U.S. Patent 6,279,069) teaches an interface for flash EEPROM memory arrays.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to T. Richard Lei whose telephone number is 703-305-4828. The examiner can normally be reached on 8:30 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dwayne Bost can be reached on 703-305-4778. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-5403 for regular communications and 703-308-5403 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

March 12, 2003

THANH CONG LE
PRIMARY EXAMINER 3/20/03